# UNIVERSITY OF SWAZILAND 

## Faculty of Science

Department of Computer Science
MAIN EXAMINATION December 2011

Title of Paper: LOGIC FOR COMPUTER SCIENCE
Course Number:CS235
Time Allowed: $\mathbf{3}$ hours Total Marks: 100
Instructions to candidates:
This question paper consists of SIX (6) questions. Answer any FOUR (4) questions. Marks are indicated in square brackets. All questions carry equal marks.

## SPECIAL REOUIREMENTS:

NO CALCULATORS ALLOWED

THIS EXAMINATION PAPER SHOULD NOT BE OPENED UNTIL PERMISSION HAS BEEN GRANTED BY THE INVIGILATOR

## OUESTION 1

a) i) State 2 limitations of truth tables.
ii) List 4 areas of application of Logic in Computer Science.
b) Explain the difference between the following terms:
i) Propositional logic syntax and propositional logic semantics.
ii) Argument and Conclusion
iii) Atomic and Compound propositions [2 each]
c) Using laws of equivalence, rewrite the proposition $\neg(\mathbf{A} \wedge \mathbf{B} \vee \neg(\neg \mathbf{A} \rightarrow \mathrm{B}))$ to one with fewer brackets and connectives.
d) Using laws of equivalence to show that the following are equivalent:
i) $P \wedge(\neg P \vee Q) \equiv \neg P \wedge(P \vee Q)$
ii) $(P \wedge \neg Q) \vee R \equiv(P \vee R) \wedge(P \vee \neg Q)$

## QUESTION 2

a) i) Using truth tables, show that $(A \vee B) \rightarrow C$ is equivalent to $(A \rightarrow C) \wedge(B \rightarrow C)$ [5]
ii) From the truth table of i) above, determine the Conjunctive Normal Form (CNF) and the Disjunctive Normal Form (DNF) of (AVB) $\rightarrow$ C
b) Three boys, Melusi, Brian and Nkosi are caught, suspected of breaking the glass in a lab.

- Melusi says: "Brian did it; Nkosi is innocent".
- Brian says: "If Melusi is guilty then so is Nkosi".
- Nkosi says: "I didn't do it; one of the others did".
i) Are the statements consistent?
ii) Assuming that everyone is innocent, who told lies? [2]
iii) Assuming that everyone's statement is true, who is innocent and who is guilty?[2]
c) Given $(\mathbf{A} \vee \mathbf{B}) \rightarrow \square \mathbf{C}, \neg \mathbf{C} \rightarrow \mathbf{D}, \mathbf{A}$ prove or deduce $\mathbf{D}$


## QUESTION 3

a) With the aid of a clear diagram(s), briefly describe the difference between Combinational and Sequential logic circuits.
b) A traditional children's riddle concerns a farmer who is traveling with a sack of rye, a goose, and a mischievous dog. The farmer comes to a river that he must cross from east to west. A boat is available, but it only has room for the farmer and one of his possessions. If the farmer is not present, the goose will eat the rye or the dog will eat the goose.
We wish to design a circuit to emulate the conditions of this riddle. A separate switch is provided for the farmer, the rye, the goose and the dog. Each switch has two states, depending on whether the corresponding object is on the east bank or the west bank of the river. The rules of play stipulate that no more than two switches be moved at a time and that the farmer must move (to row the boat) each time switch are moved. The switch for the farmer provides logic signal $F$, which is high if the farmer is on the east bank and low if he is on the west bank. Similar logic signals ( $G$ for the goose, $D$ for the $\operatorname{dog}$ and R for the rye) are high if the corresponding object is in the east bank and low if it is on the west bank.
Construct the truth table for this riddle and hence find the Boolean logic expression based on the sum of products approach for a logic signal A (Alarm) that is high any time the rye or the goose are in danger of being eaten. Repeat for the product of sums approach.
c) Convert the following into SOP form and minimize using the Karnaugh map method. $\mathrm{F}=A \bar{B}+A B C+B C \bar{D}$
d) Interpret the circuit diagram in fig 1 and then complete the truth table that follows.[6]


Fig 1:

| $\boldsymbol{A}$ | $\boldsymbol{B}$ | $\boldsymbol{C}$ | $\boldsymbol{C}$ | 0 |
| :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 1 | 0 |  |
| 0 | 1 | 1 | 1 |  |
| 1 | 0 | 0 | 0 |  |
| 1 | 1 | 0 | 0 |  |

## QUESTION 4

a) The state of a CPU register is 0101100101 , what is its content if it represents:
i. Digits in BCD
ii. Digits in two's compliment
b) Simplify the followingexpression using Boolean theorems.
$(\overline{A+B})+\bar{C}$
c) i) Briefly explain the difference between the Karnaugh map method and the QuineMcCluskey method.
ii) Minimize the function $f(A, B, C, D)=\sum m(0,1,2,3,5,7,8,10,12,13,15)$ using the Quine-McCluskey method.

## QUESTION 5

a) Convert
i) $1023.0625_{10}$ to binary [4]
ii) 769 to BCD
iii) 157 to Hexadecimal
iii) 157 to Hexadecimal [3]
iv) 11011.1011 to decimal [3]

Show all the working.
b) Find the 2 's compliment representation of -91 . Explain why the 2 's complement arithmetic is commonly used as compared to other methods.
c) State 2 examples of situations where floating-point number representation is useful and often used.
d) With the aid of well-labeled circuit diagrams, distinguish between the Half adder and full adder circuits in the way they operate.

## OUESTION 6

a) Describe the difference between synchronous and asynchronous circuits.
b) With the aid of clear diagrams explain the operations of the following:
i) 4 to 1 - line Multiplexer
ii) 3 to 8 - line Decoder
c) "If the program is running then there is at least 250 K of RAM." Which of the following are equivalent to this statement?
i) If there is at least 250 K of RAM then the program is running.
ii) If there is less than 250 K of RAM then the program is not running. .
iii) The program will run only if there is at least 250K of RAM.
iv) If the program is not running then there is less than 250 K of RAM.
v) A necessary condition for the program to run is that there are at least 250 K of RAM.
d) Flip flops can be implemented using R-S, D-type or J-K. Explain the behavior of J-K flip-flop. What additional logic is required to convert a J-K flip-flop into a D-type flip flop?

