University of Swaziland

Department of Computer Science

Supplementary Examination

July 2012

TITLE OF PAPER: COMPUTER ORGANISATION I

COURSE NUMBER: CS 241

TIME ALLOWED: 3 HOURS

INSTRUCTIONS:

ANSWER ONE QUESTION FROM SECTION A ANSWER THREE QUESTIONS FROM SECTION B

This examination paper should not be opened until the invigilator grants permission.

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SECTION A

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Question 1 (COMPULSORY)

Explain each of the following terms:	
i. Translator	[2]
ii. Interpreter	[2]
iii. Virtual machine	[2]
iv. What is the difference between translation and interpretation?	[2]
Draw a clearly labelled diagram of the CPU showing the following: AL	U,
accumulator, I/O.	[8]
Explain Moore's Law with an example from each of the following:	
i. Number of transistors in a CPU	[2]
ii. Memory capacity	[2]
Convert the integer 2063 into the following radix:	
i hin	[2]
1. 011,	[~]
	 Explain each of the following terms: Translator Interpreter Virtual machine What is the difference between translation and interpretation? Draw a clearly labelled diagram of the CPU showing the following: AL accumulator, I/O. Explain Moore's Law with an example from each of the following: Number of transistors in a CPU Memory capacity Convert the integer 2063 into the following radix:

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SECTION B (ANSWER ANY THREE QUESTIONS FROM THIS SECTION)

Qu	estion	2	
A.	The in single	equality $(m+r+1) \le 2^r$ determines the limit of check bits needed to co- bit errors.	orrect
	i.	Create a table for the hamming codes for the following message size indicating: message length, codeword length, number of check-bits a percentage of bits wasted for the following word sizes: 16, 32, 128, 2010	s ind the 320 512
			[15]
	ii.	Construct the Hamming code for the following 16-bit message 1111000010101110.	[5]
B. C.	Define Define	e the principle that determines the success of cache memory. e cache hit ratio, miss ratio.	[3] [2]

Question 3

Α.	Draw a half adder	[5]
В.	Describe a multiplexer with the aid of a diagram	[5]



C. The above circuit diagram shows a *full adder*. Write out a truth table showing values of the sum and carry out for all the possible combinations of A, B and carry in.

The gates marked X are exclusive or (XOR) gates; those marked A are AND gates; the gate marked O is an OR. [10]

D. Briefly describe the following storage devices: CDROM, DVD [5]

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Question 4

Α.	Distinguish between synchronous and asynchronous buses	[9]
B.	Describe with the aid of an illustration:	
	i) Decoderii) SR latch	[8] [8]

Question 5

- A. How long does it take to read a disk with 10,000 cylinders, each containing four track of 2048 sectors? First, all sectors of track 0 are to be read starting at sector 0, then all sectors of track 1 starting at sector 0, and so on. The rotation time is 10msec, and a seek takes 1msec between adjacent cylinders and 20msec for the worst case. Switching between tracks of a cylinder can be done instantaneously. [10]
- B. To be able to fit 133 minutes worth of video on a single-sided single-layer DVD, a fair amount of compression is required. Calculate the compression factor required. Assume that 3.5 GB of space is available for the video track, that the image resolution is 720 x 480 pixels with 24-bit color, and images are displayed at 30 frames /sec.[10]

C. Represent the following number using 2 s complement -120	[5]
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END OF EXAM -----

----- TOTAL: 100 MARKS

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