## UNIVERSITY OF SWAZILAND MAIN EXAMINATION 2012

### FACULTY OF SCIENCE

### DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING

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TITLE OF PAPER:ANALOG DESIGN II / ANALOGUE ELECTRONICS IICOURSE NUMBER:EE323 / E442TIME ALLOWED:THREE HOURS

### INSTRUCTIONS

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- 1. Read each of the FIVE questions carefully
- 2. Answer any FOUR questions.
- 3. Each question carries **25 marks**
- 4. Marks for each section are shown on the right-hand margin

THIS PAPER HAS SIX PAGES INCLUDING THIS PAGE.

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(a)	(i)	What is a feedback circuit?		
			[2marks]	
	(ii)	Derive an expression for the gain of a negative voltag amplifier.	ge feedback	
			[8marks]	
(b)	(i)	With the aid of a neat diagram describe the action of	an emitter follower.	
()	(1)			
			[5marks]	
	(ii)	What is Darlington Amplifier?		
			[3 marks]	
(c)	When negative voltage feedback is applied to an amplifier of gain 100, the overall gain falls to 50.			
	(i)	Calculate the fraction of the output voltage feedback.		
			[3marks]	
••		If this fraction is maintained, calculate the value of th required if the overall stage gain is to be 75.	e amplifier gain	

[4 marks]

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(a)	(i)	What is an oscillator?	[2marks]
	(ii)	With the aid of a neat diagram, explain the operation	• •
(b)	(i)	Why do we use three RC sections in an RC oscilla	tor? [3marks]
	(ii)	With the aid of a neat circuit diagram, explain the c phase shift oscillator and name three advantages oscillator.	-
			[9marks]
(c)		e phase shift oscillator circuit, R1 = R2 = R3 = 1MΩ and C1 = C2 = C3 = 68pF. sulate the frequency at which the circuit oscillates.	

[3 marks]

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- (a) A power amplifier supplies 50W to an 8-ohm speaker. Find
  - (i) the a.c. output voltage.
  - (ii) the a.c. output current.

[4 marks]

- (b) (i) Classify power amplifiers according to their mode of operation.
  - (ii) Draw the circuit of a class A power amplifier.
  - (iii) Explain the operation of class A and class B power amplifiers in terms of their a.c. load lines.

[13 marks]

- (c) A class A transformer coupled power amplifier has zero signal collector current of 50mA. If the collectors supply voltage is 5V, find:
  - (i) The maximum a.c. power output
  - (ii) The power rating of transistor
  - (iii) The maximum collector efficiency

[8 marks]

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(i) What is a multivibrator?		[2marks]
(ii)	Explain the principle on which it works.	[ZIIIdi Ko]
*		[4 marks]
With	orator.	
		[13 marks]
	(ii)	

(c) In the astable multivibrator,  $R2 = R3 = 10K\Omega$  and  $C1 = C2 = 0.01\mu$ F. Determine the time, period and frequency of the wave it produces.

[6 marks]

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(a)	(i)	List the different types of Field Effect Transistors.	[3marks]	
	(ii)	Explain the construction and working of a JFET.	[10 marks]	
(b)	Write short notes on the following:			
	(i)	Advantages of JFET	[3marks]	
	(ii) Difference between JFET and Bipolar Junction Transistor			
			[3 marks]	
(c)	1.5m	In a self-biased n-channel JFET circuit, the operating point is to be set at $I_D$ : 1.5mA and $V_{DS}$ = 10V. The JFET parameters are $I_{DSS}$ = 5mA and $V_P$ = -2V. Calculate the values of $R_S$ and $R_D$ , given that $V_{DD}$ = 20V.		

[6 marks]